

ABSTRACT OF THE DISCLOSURE

The improved image filter circuit for generating unsharp image signals used to subject image signals to dodging treatment in an image processing apparatus for image processing the image signals comprises an IIR type filter for carrying out filtering treatment to generate the unsharp image signals; and a FIFO type field memory for delaying image signals which are not subjected to the filtering treatment at the IIR filter for a time corresponding to the delay time of image signals which have been subjected to the filtering treatment at the IIR type filter. The image filter circuit of the present invention, which obtains the unsharp image signals corresponding to an unsharp mask for obtaining a dodging effect, can be made small in size with a simplified circuit arrangement.

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